



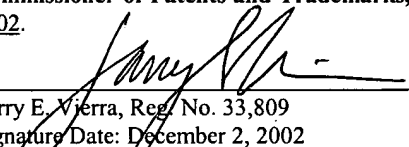
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

7/Election/6  
T. step 106  
12-14-02

In re Application	)	<u>PATENT APPLICATION</u>
Inventors:	)	
Mark A. Lemkin, et al.	)	
SC/Serial No.:	)	Art Unit: 2856
09/765,521	)	
Filed:	)	Examiner: Bellamy, Tamiko D.
Jan. 18, 2001	)	
Title: POSITION SENSING WITH IMPROVED	)	
LINEARITY	)	

**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8**

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner of Patents and Trademarks, Washington, D.C. 20231, on December 2, 2002.

 (Attorney Signature)  
Larry E. Vierra, Reg. No. 33,809  
Signature Date: December 2, 2002

RESPONSE TO OFFICE ACTION UNDER 37 C.F.R. §1.111

Commissioner of Patents  
Washington, D.C. 20231

Sir:

This RESPONSE is in reply to the Office action mailed September 30, 2002.

Amendments

In the Claims

Claims 26 - 48 are withdrawn from consideration.

17. The interface of claim 1, wherein at least one of said sense capacitors is formed as part of [An integrated circuit formed on a semiconductor substrate, comprising:]  
a micromechanical structure formed in or on said substrate; and  
said [a] position detection circuitry is formed in and on said substrate, and [including] includes an

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